

FIG. 1 - Prior Art

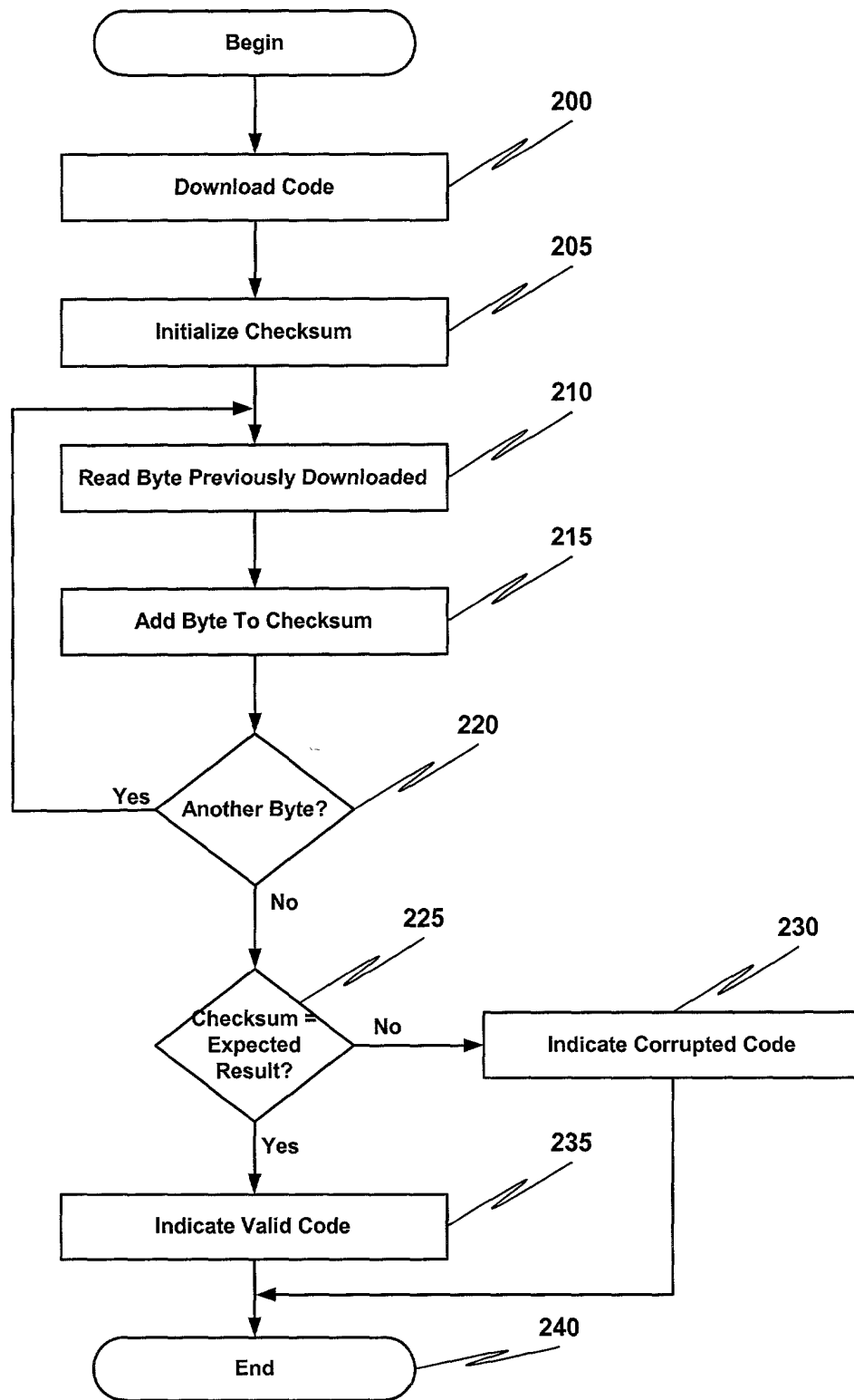


FIG. 2 - Prior Art

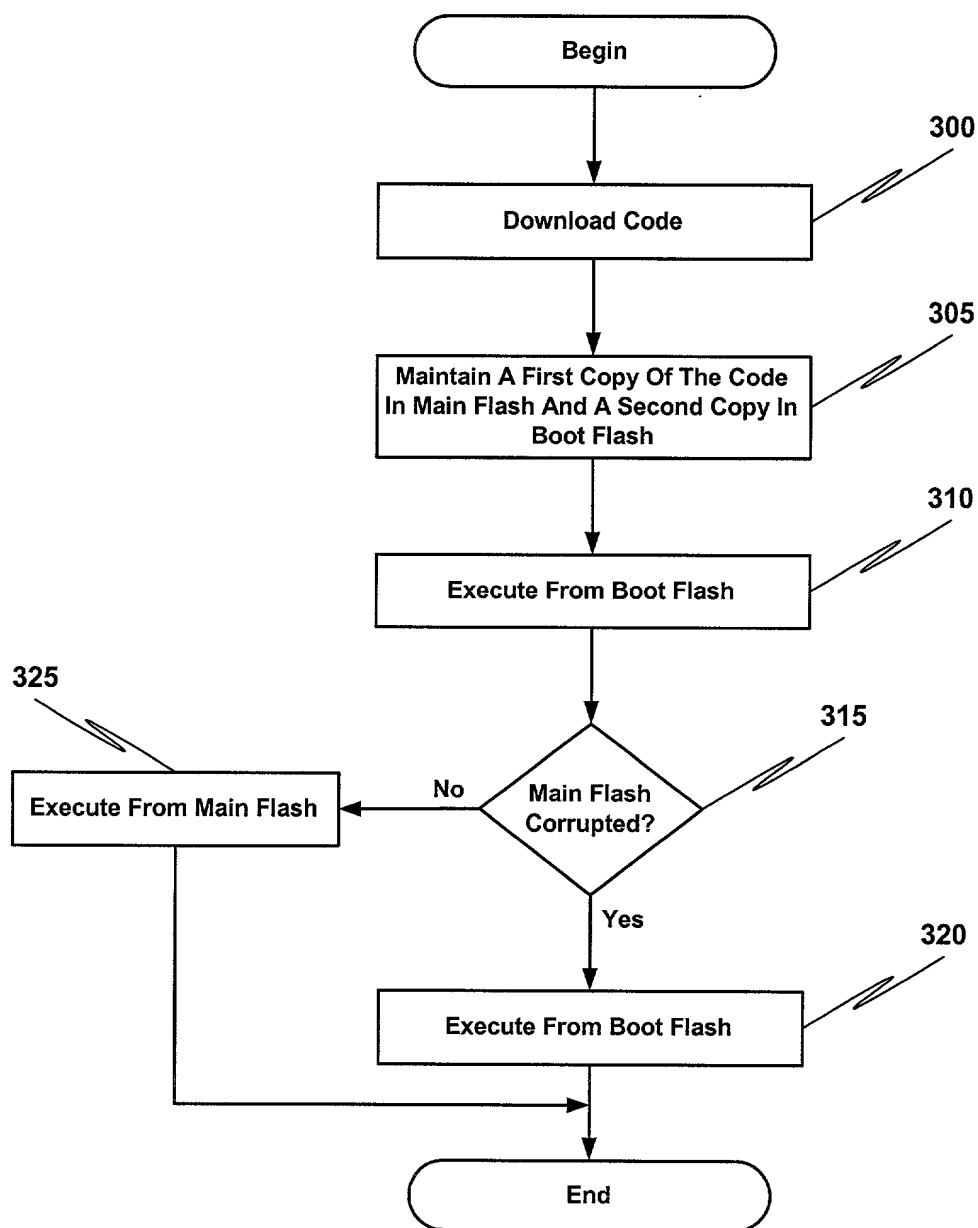


FIG. 3 - Prior Art

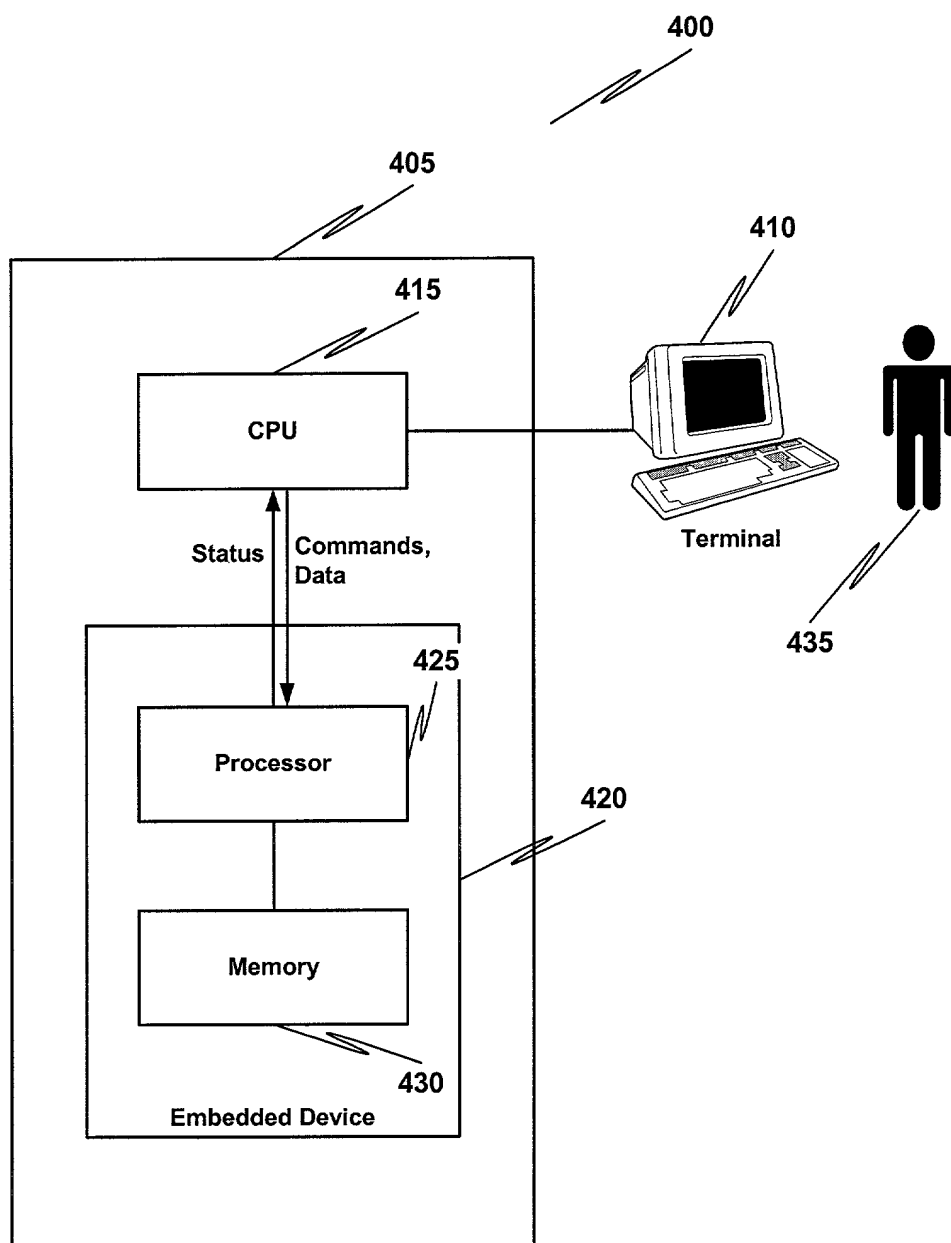


FIG. 4

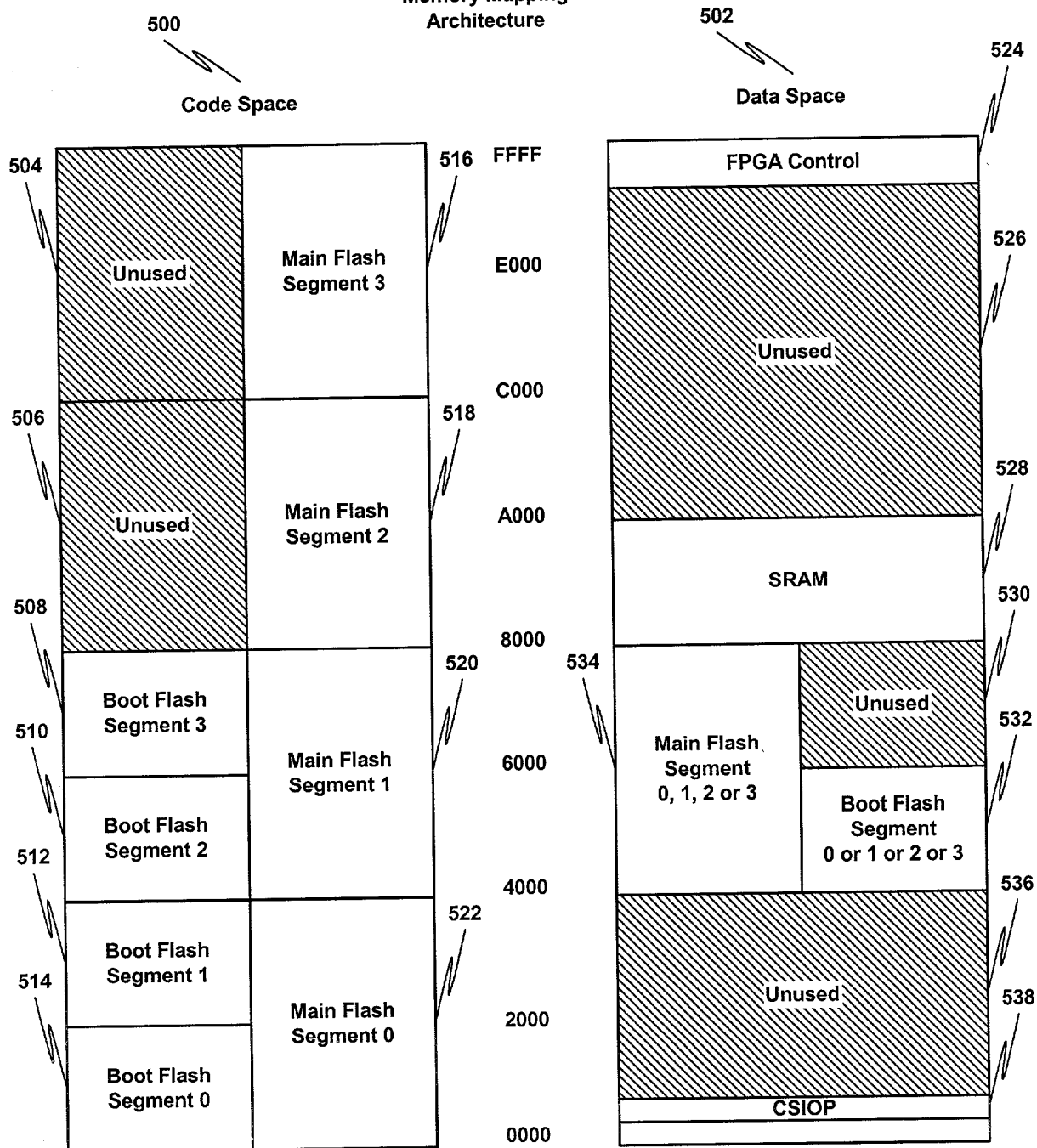
Memory Mapping
Architecture

FIG. 5

600

Page Register

Bit	Data	Note
0..2	Main Flash Segment Select	8 Segments Total
3..4	Boot Flash Segment Select	4 Segments Total
5	Flash Code Select	1: Main Flash Code 0: Boot Flash Code
6..7	Unused	

605

610

615

620

FIG. 6A

625

VM Register

Bit	Data
0	SRAM Is Code
1	Boot Flash Is Code
2	Main Flash Is Code
3	Boot Flash Is Data
4	Main Flash Is Data
5..6	Unused
7	Enable PIO

630

635

640

645

650

655

660

FIG. 6B

700

Main Flash Segment 0 = (Device Space=Code) & (Flash Code Select=Main) & (address in 0000..3FFF)
OR (Device Space=Data) & (Main Flash Segment Select=0) & (address in 4000..7FFF))

Main Flash Segment 1 = (Device Space=Code) & (Flash Code Select=Main) & (address in 4000..7FFF)
OR (Device Space=Data) & (Main Flash Segment Select=1) & (address in 4000..7FFF))

Main Flash Segment 2 = (Device Space=Code) & (Flash Code Select=Main) & (address in 8000..BFFF)
OR (Device Space=Data) & (Main Flash Segment Select=2) & (address in 4000..7FFF))

Main Flash Segment 3 = (Device Space=Code) & (Flash Code Select=Main) & (address in C000..FFFF)
OR (Device Space=Data) & (Main Flash Segment Select=3) & (address in 4000..7FFF))

FIG. 7A

705

Boot Flash Segment 0 = (Device Space=Code) & (Flash Code Select=Boot) & (address in 0000..1FFF)
OR (Device Space=Data) & (Boot Flash Segment Select=0) & (address in 4000..5FFF))

Boot Flash Segment 1 = (Device Space=Code) & (Flash Code Select=Boot) & (address in 2000..3FFF)
OR (Device Space=Data) & (Boot Flash Segment Select=1) & (address in 4000..5FFF))

Boot Flash Segment 2 = (Device Space=Code) & (Flash Code Select=Boot) & (address in 4000..5FFF)
OR (Device Space=Data) & (Boot Flash Segment Select=2) & (address in 4000..5FFF))

Boot Flash Segment 3 = (Device Space=Code) & (Flash Code Select=Boot) & (address in 6000..7FFF)
OR (Device Space=Data) & (Boot Flash Segment Select=3) & (address in 4000..5FFF))

FIG. 7B

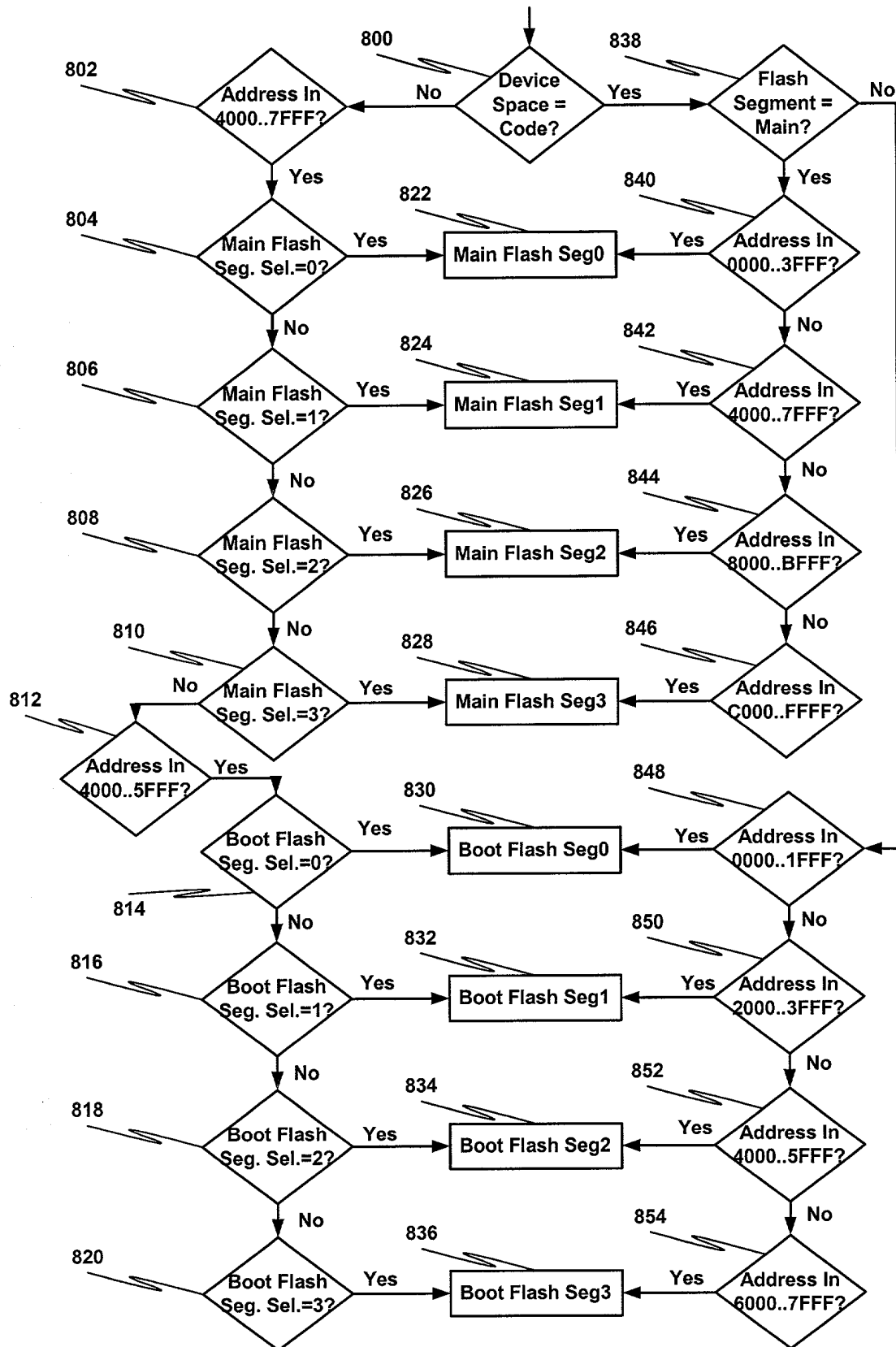


FIG. 8

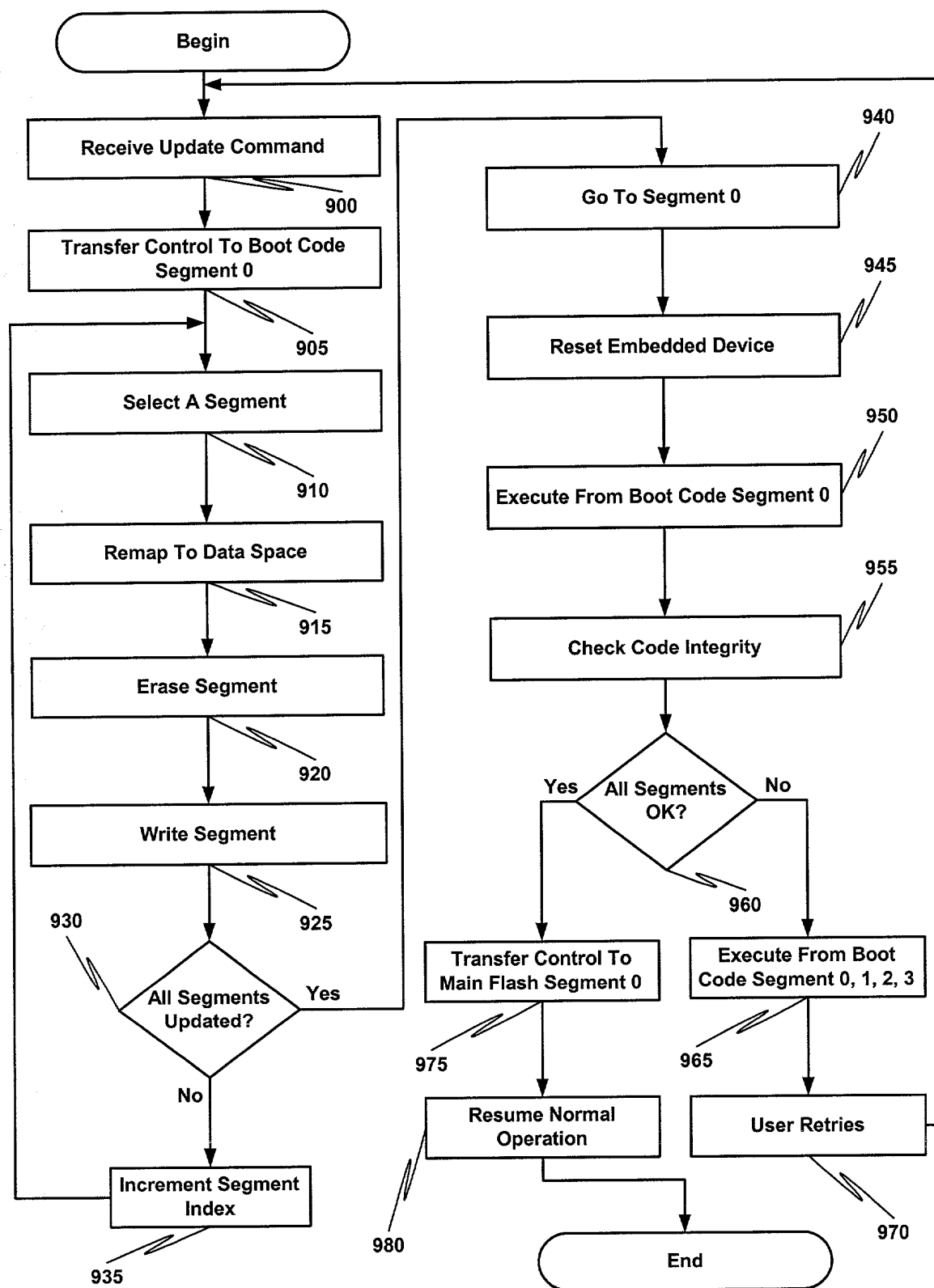


FIG. 9

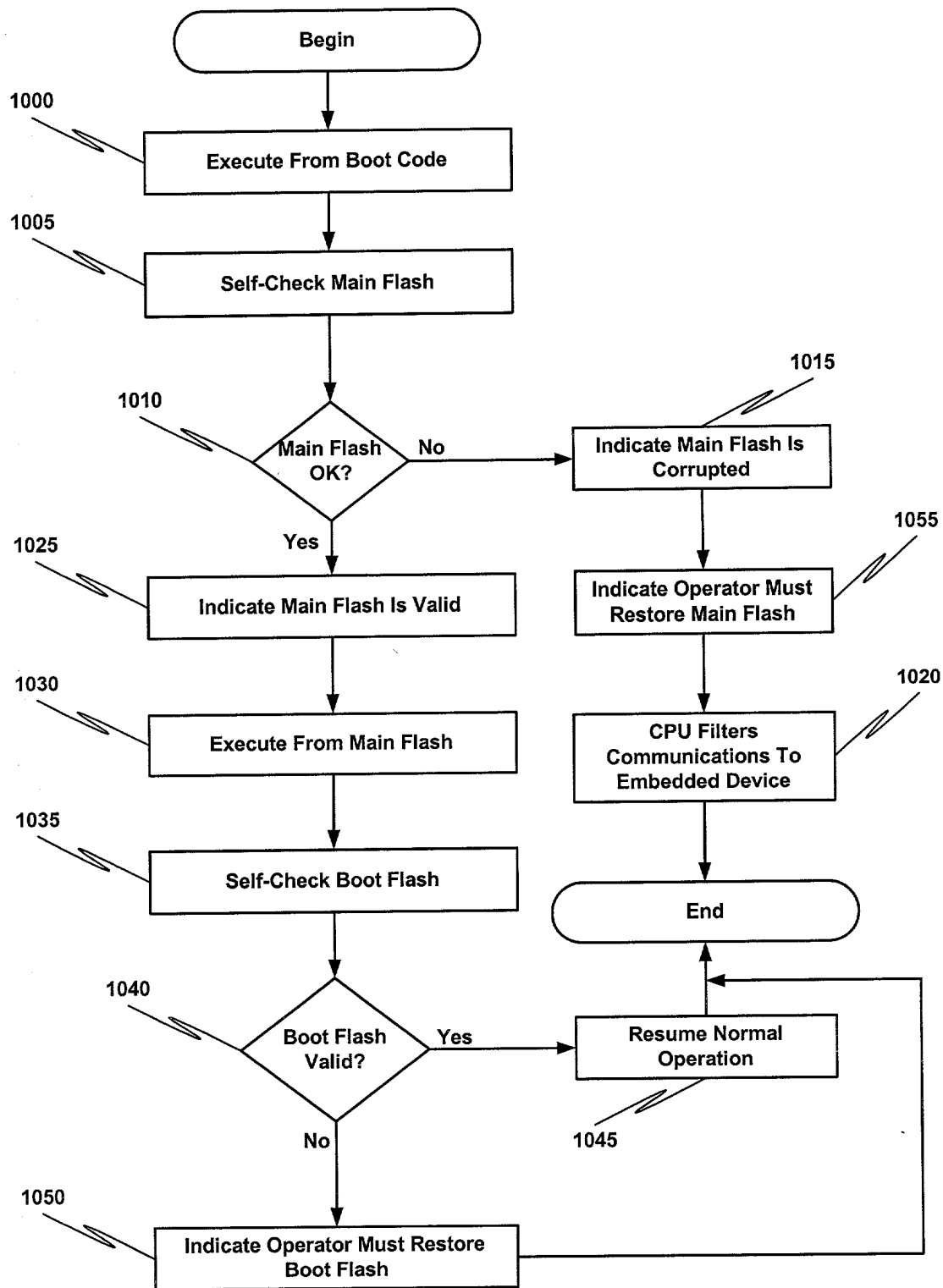


FIG. 10

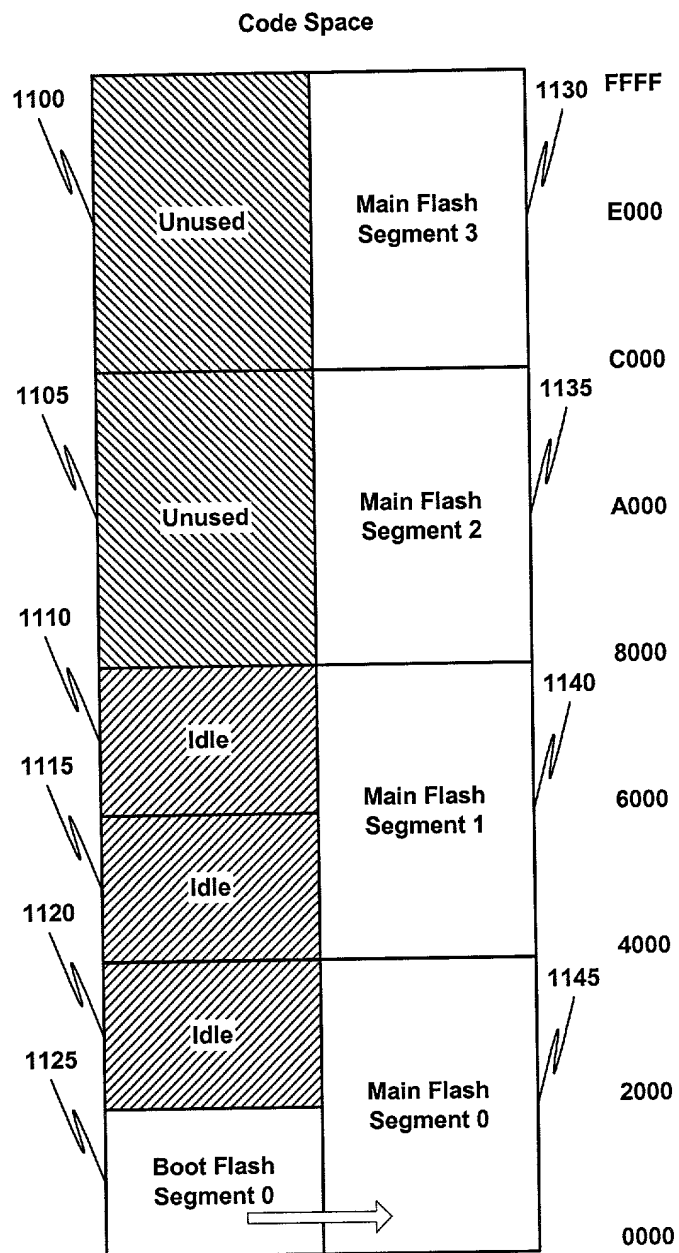


FIG. 11

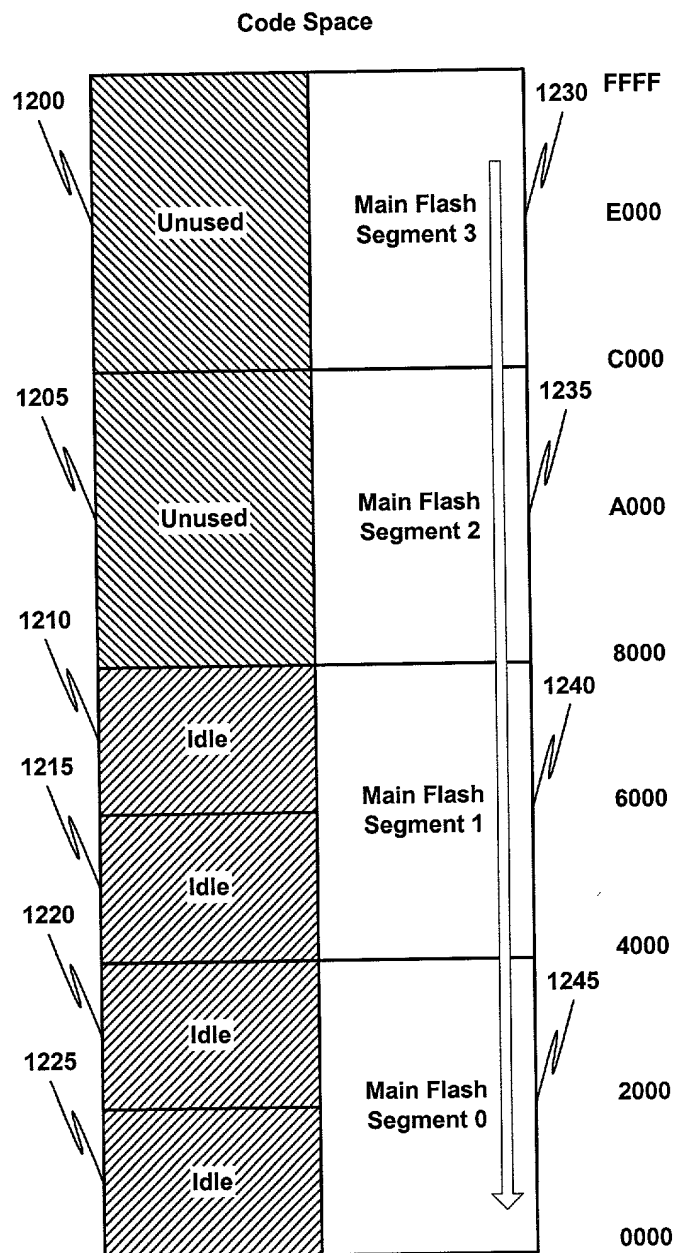


FIG. 12

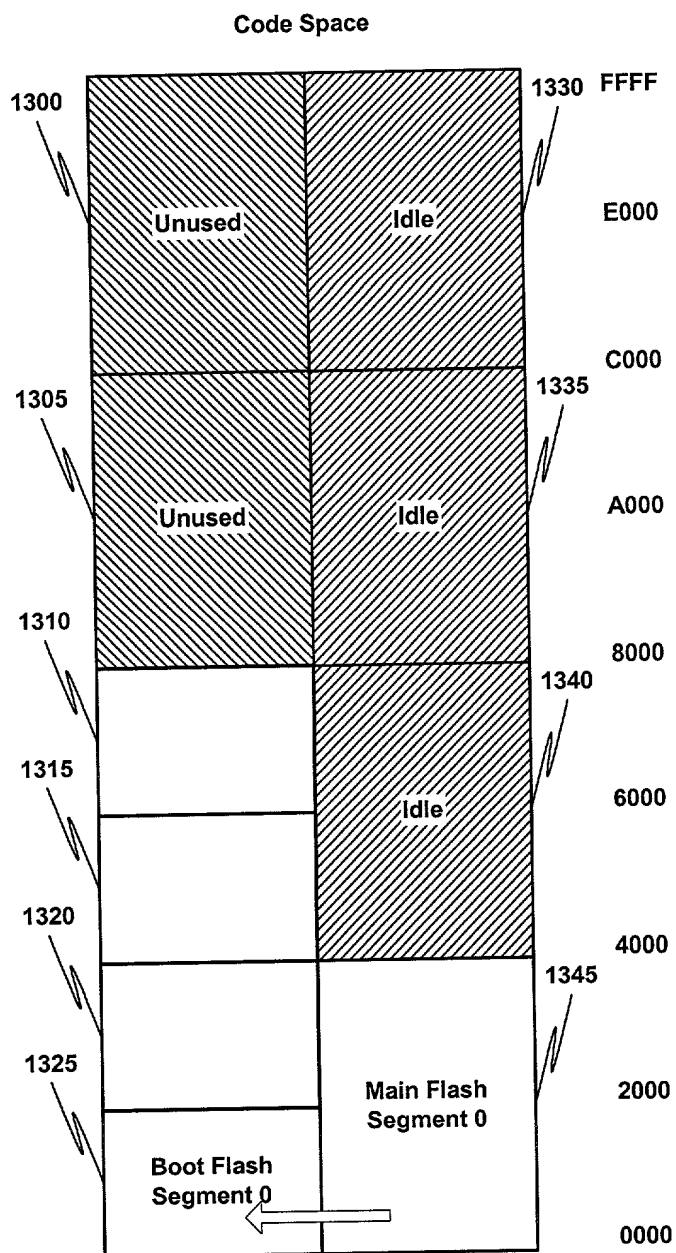


FIG. 13

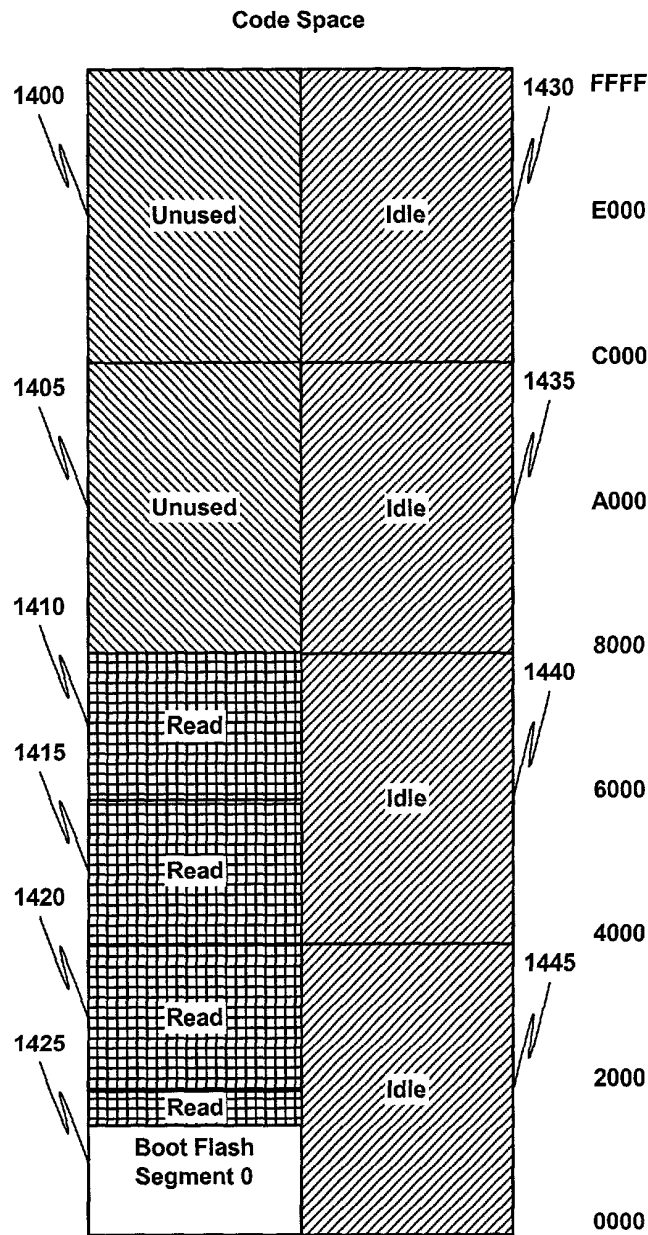


FIG. 14

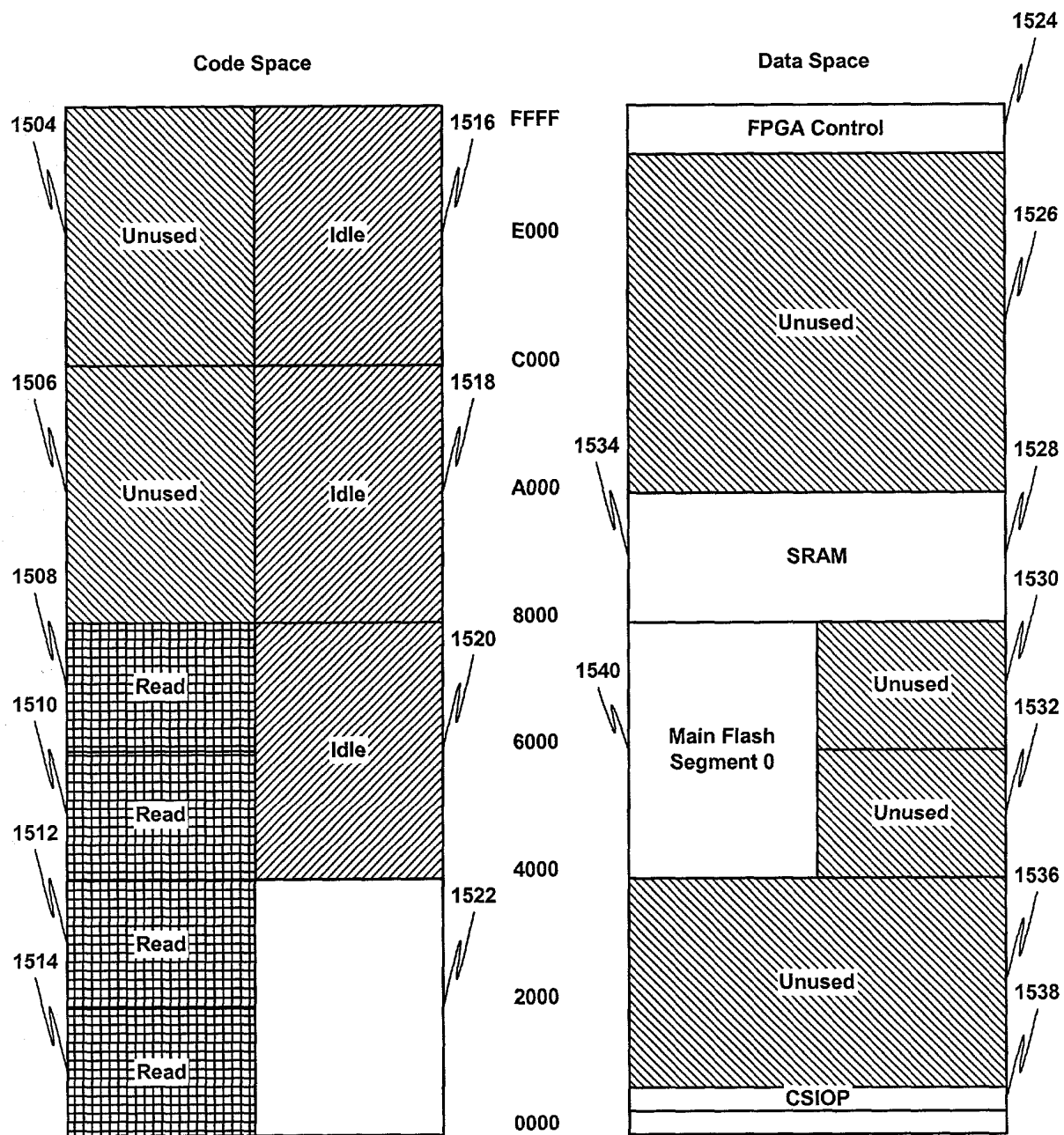


FIG. 15

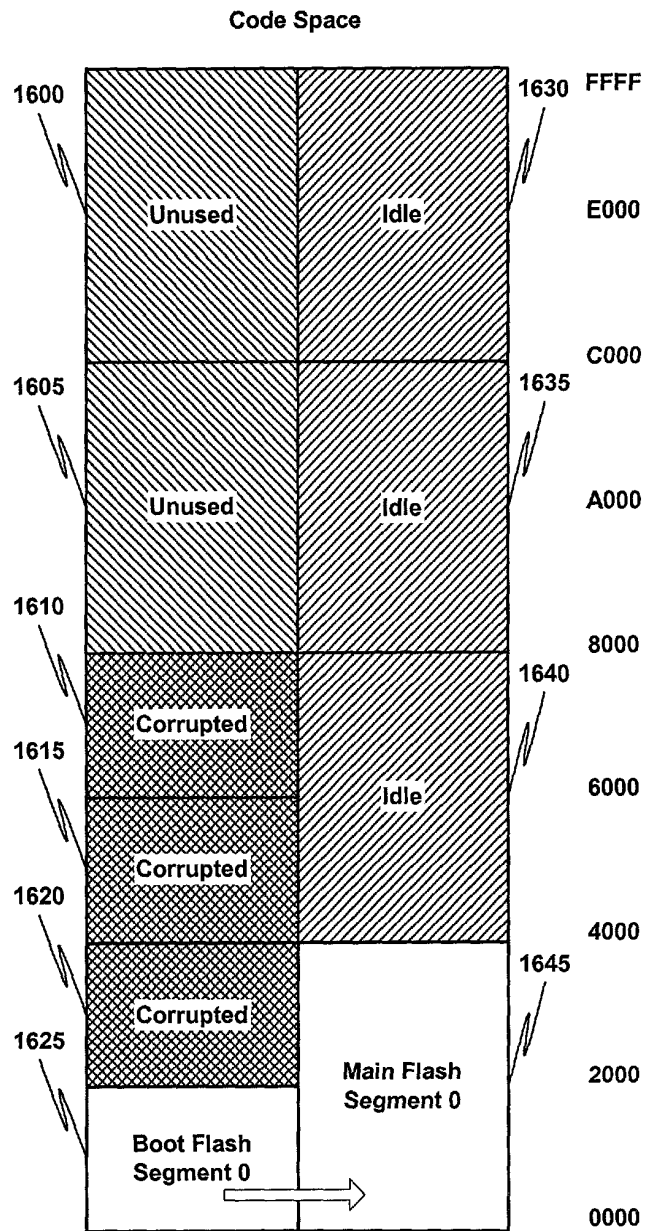


FIG. 16

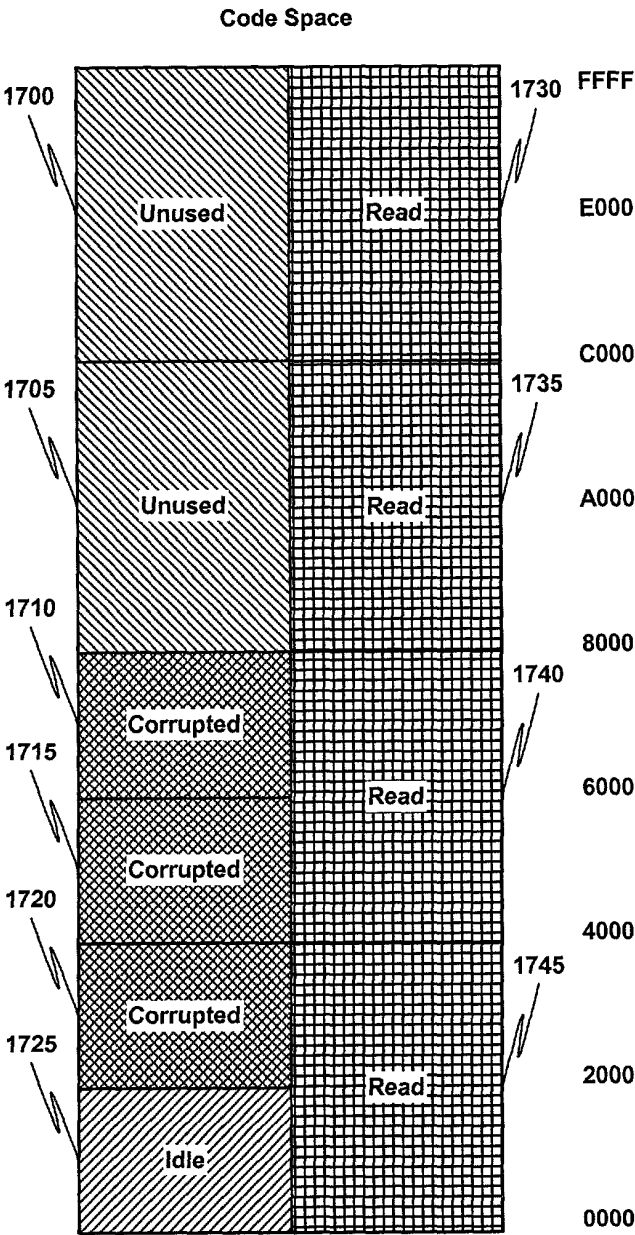


FIG. 17

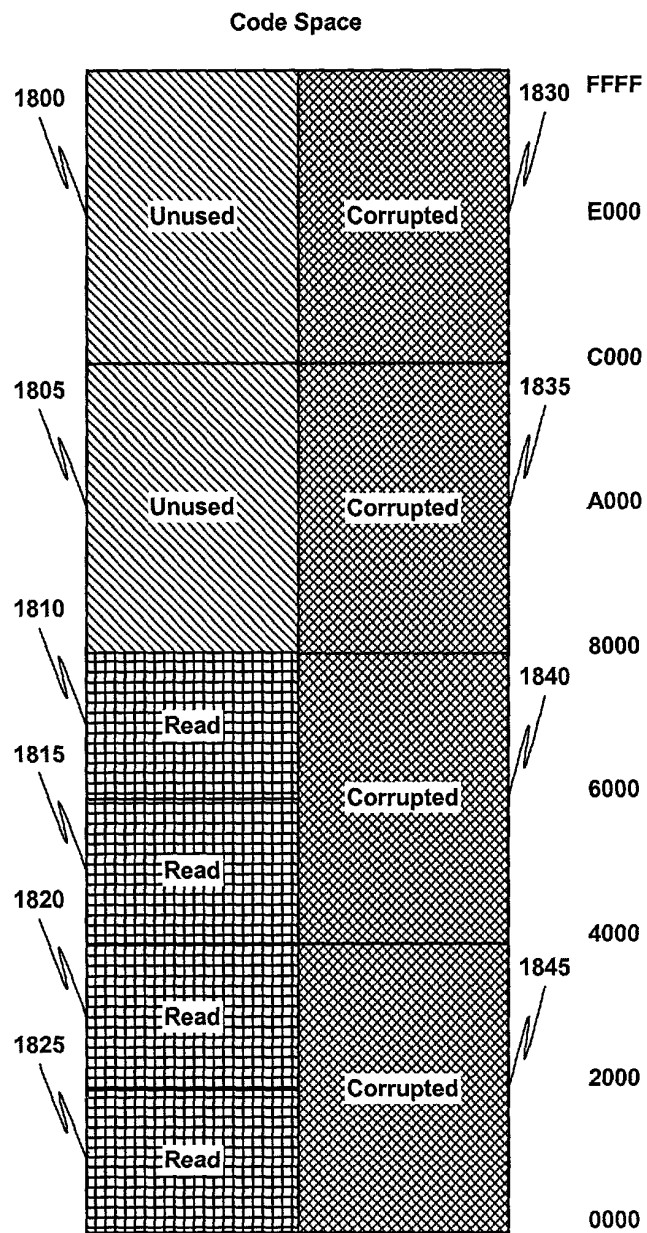


FIG. 18

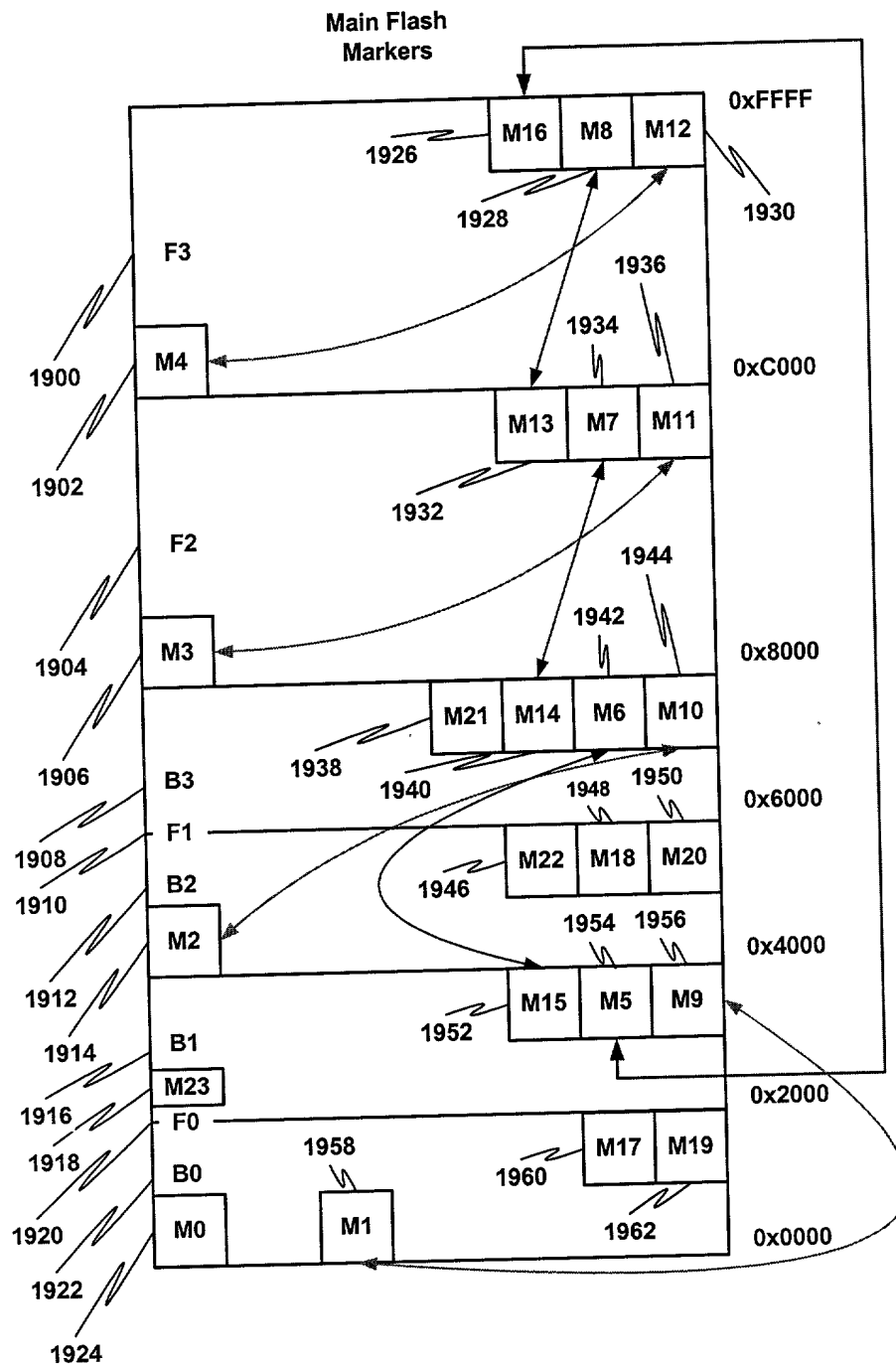


FIG. 19

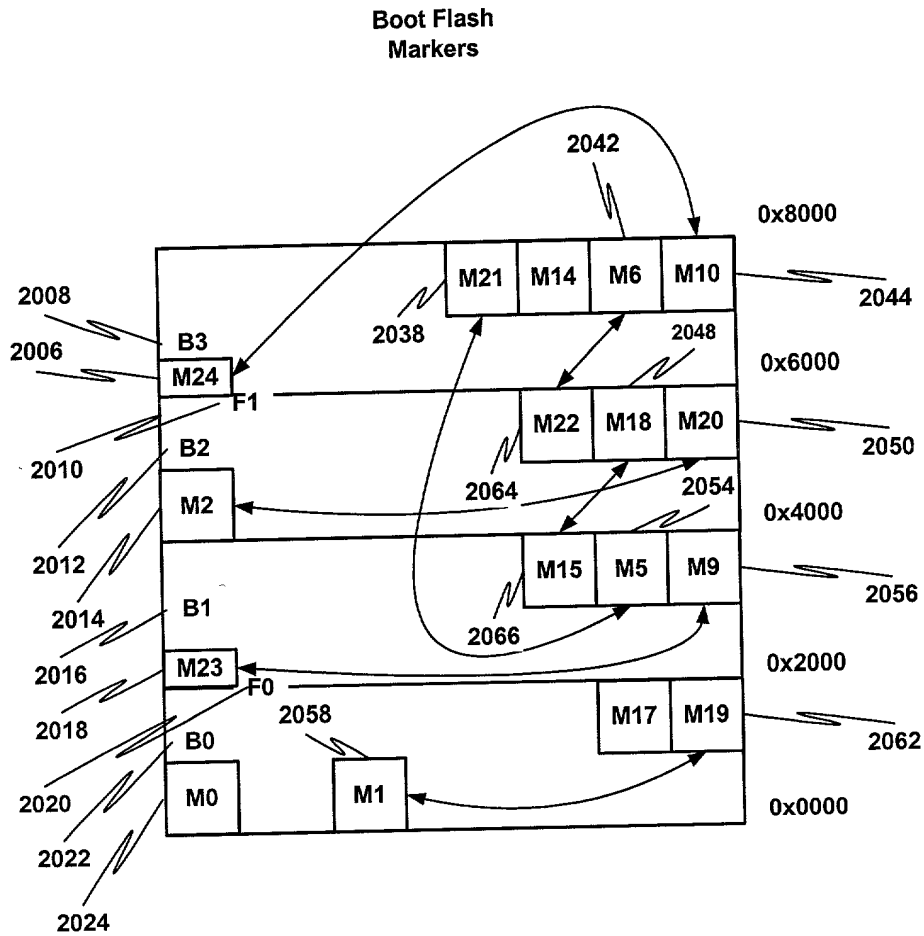


FIG. 20

Marker
Correlation

M	~M
M4	M12
M3	M11
M2	M10
M1	M9
M8	M13
M7	M14
M6	M15
M5	M16

2100

2105

FIG. 21A

M	~M
M24	M10
M2	M20
M23	M9
M1	M19
M5	M21
M18	M15
M6	M22

2110

2115

FIG. 21B

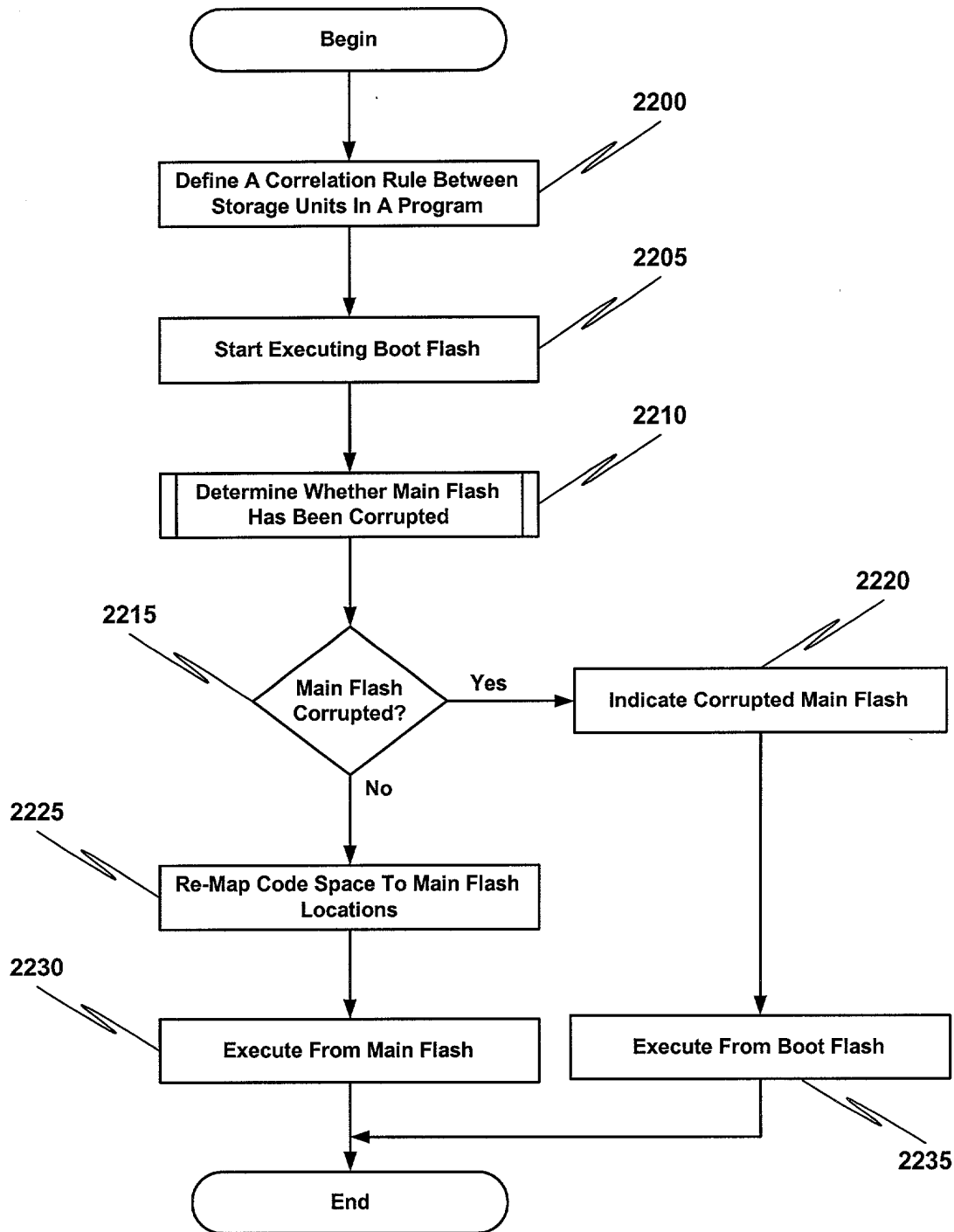


FIG. 22

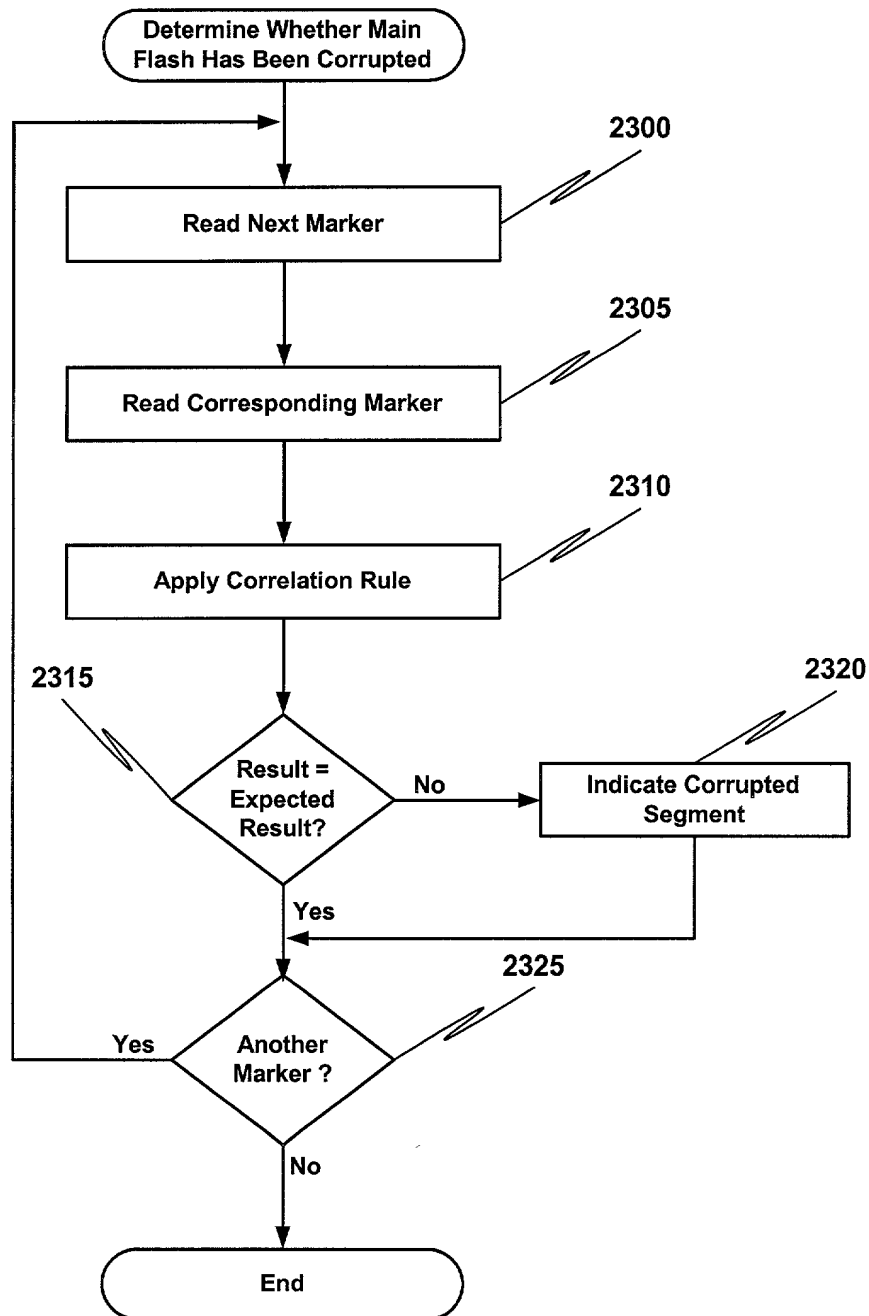


FIG. 23